

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,193,464 B2
APPLICATION NO. : 10/015887
DATED : March 20, 2007
INVENTOR(S) : Wang et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14

Line 45 please replace " v_{ovMS} " with $--v_{ovM8}--$.

Column 16 Claim 2

Line 63 please replace "pairs" with $--pair--$.

Column 17 Claim 7

Line 23 please replace "step" with $--steps--$.

Column 18 Claim 7

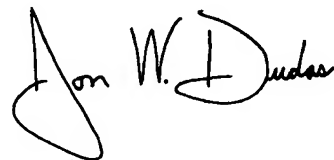
Line 3 please replace "offset voltage" with $--level\ shifted\ voltage--$.

Column 18 Claim 9

Line 9 please replace "step" with $--steps--$.

Signed and Sealed this

Tenth Day of July, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office